

FIG.1

CONTROL CHANNEL

CONTROL INFORMATION

5 FIG.2

10 DUPLEXER

11 RECEPTION SECTION

12 CONTROL CHANNEL RECEPTION SECTION

13 CONTROL CHANNEL DEMODULATION SECTION

10 14 CONTROL CHANNEL DECODING SECTION

15 DETERMINATION SECTION

16 CONTROL SECTION

17 DATA CHANNEL RECEPTION SECTION

18 DATA CHANNEL DEMODULATION SECTION

15 19 DATA CHANNEL DECODING SECTION

20 ERROR DETECTION SECTION

INFORMATION BIT DATA

21 TRANSMISSION SECTION

20 FIG.3

102 DUPLEXER

103 RECEPTION SECTION

104 CONTROL CHANNEL RECEPTION SECTION

105 CONTROL CHANNEL DEMODULATION SECTION

25 106 CONTROL CHANNEL DECODING SECTION

107 DETERMINATION SECTION

108 DATA CHANNEL RECEPTION SECTION

109 DATA CHANNEL DEMODULATION SECTION

110 DATA CHANNEL DECODING SECTION

111 ERROR DETECTION SECTION

INFORMATION BIT DATA

5 112 TRANSMISSION SECTION

113 CAPABILITY COMPARING SECTION

114 CONTROL SECTION

FIG.4 FIG.5 FIG.6

10 SLOT

FIG.5 FIG.6

FIRST PART

SECOND PART

CONTROL CHANNEL 1

CONTROL
INFORMATION 1

CONTROL CHANNEL 2

CONTROL
INFORMATION 2

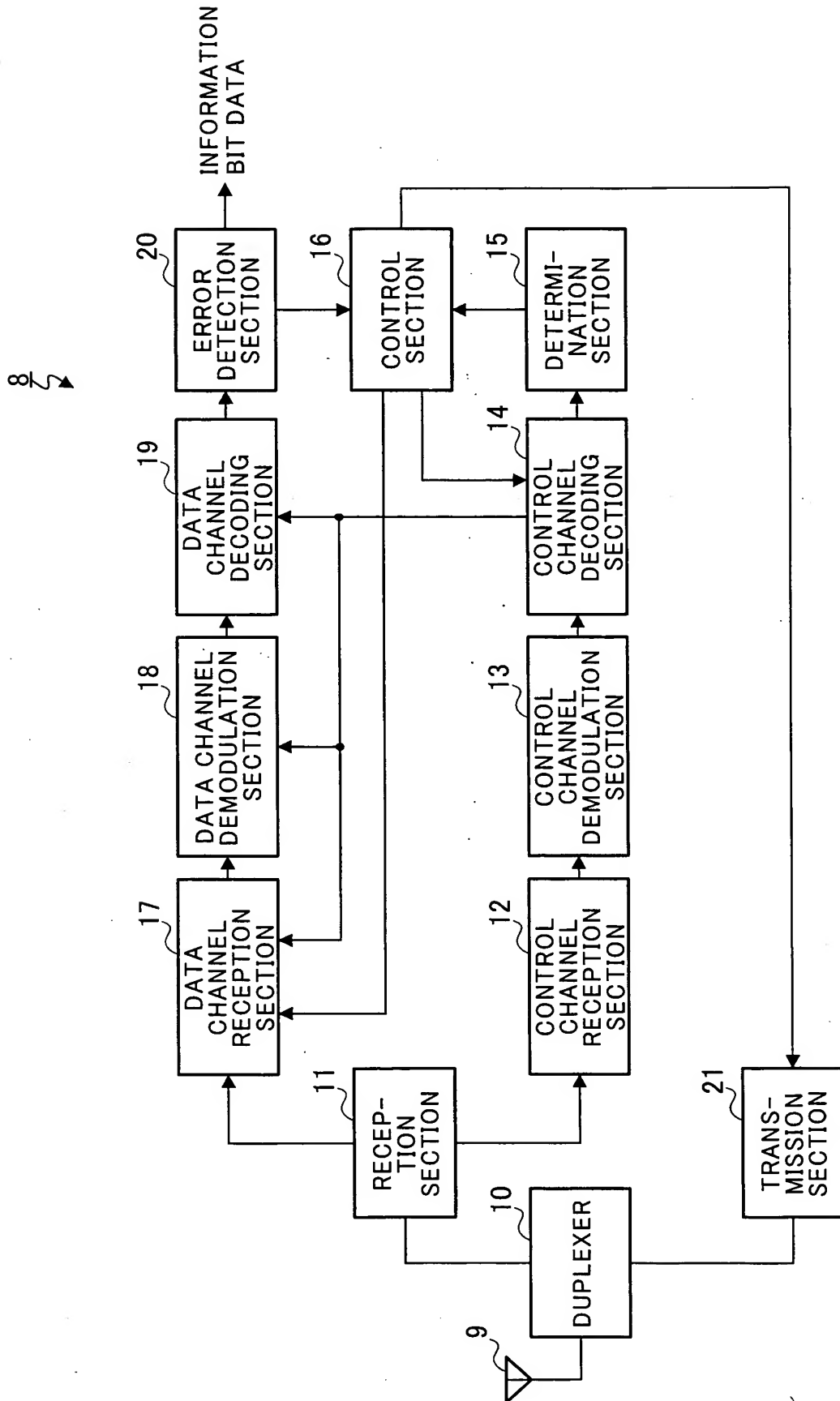
⋮

⋮

CONTROL CHANNEL N

CONTROL
INFORMATION N

FIG.1



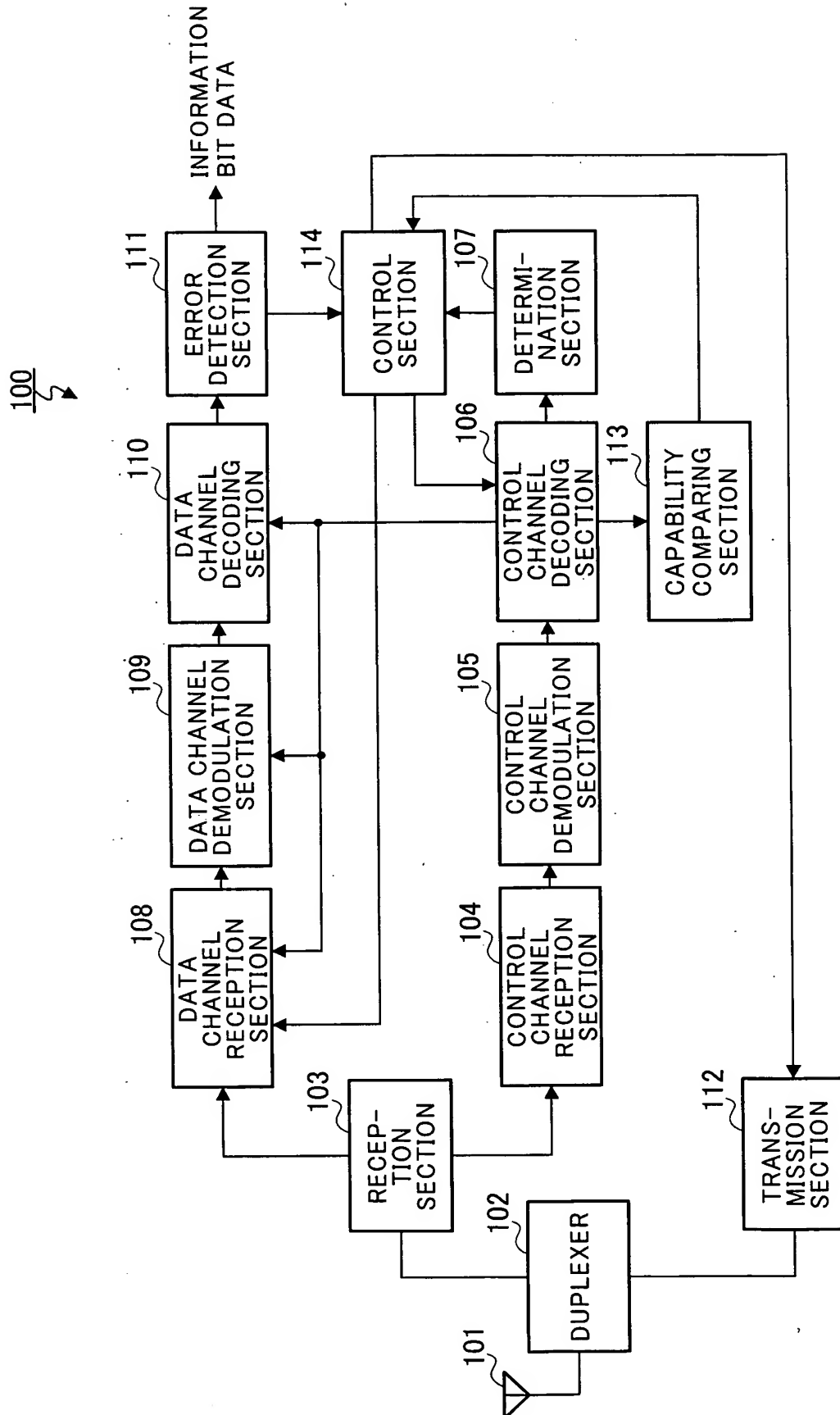


FIG.3

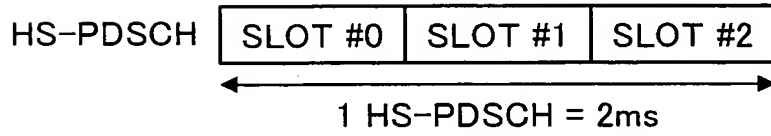


FIG.4

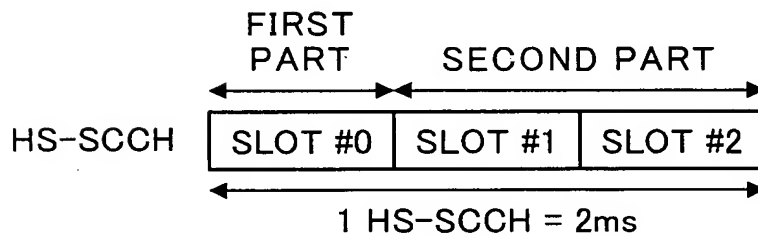


FIG.5

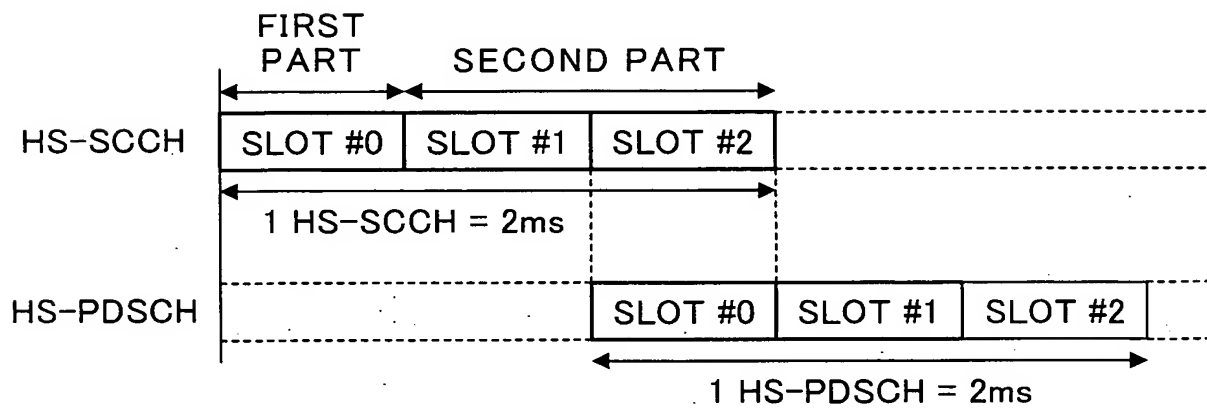


FIG.6